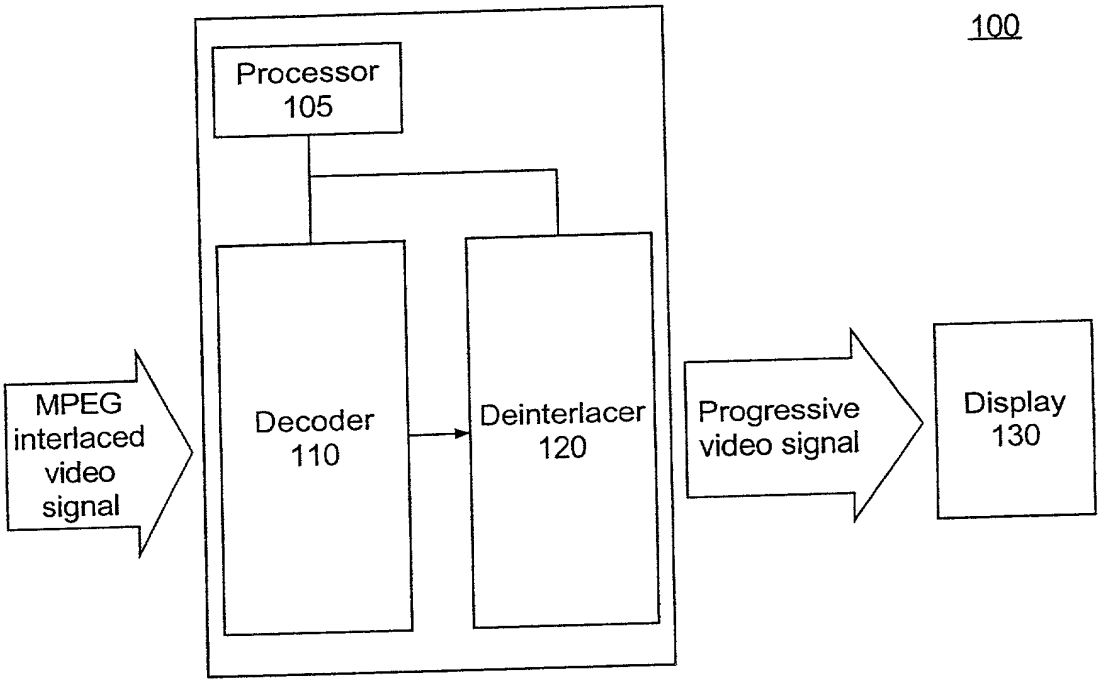
100
Processor
105
MPEG
interlaced
video
signal
Decoder
110
Deinterlacer
120
Progressive
video signal
Display
130

FIGURE 1

Top Field	Bottom Field
1X	
AO	
	2X
3X	
	BO
	4X
5X	
CO	
	6X
7X	
	DO
	8X

X = Luma Lines

O = Chroma Lines

FIGURE 2

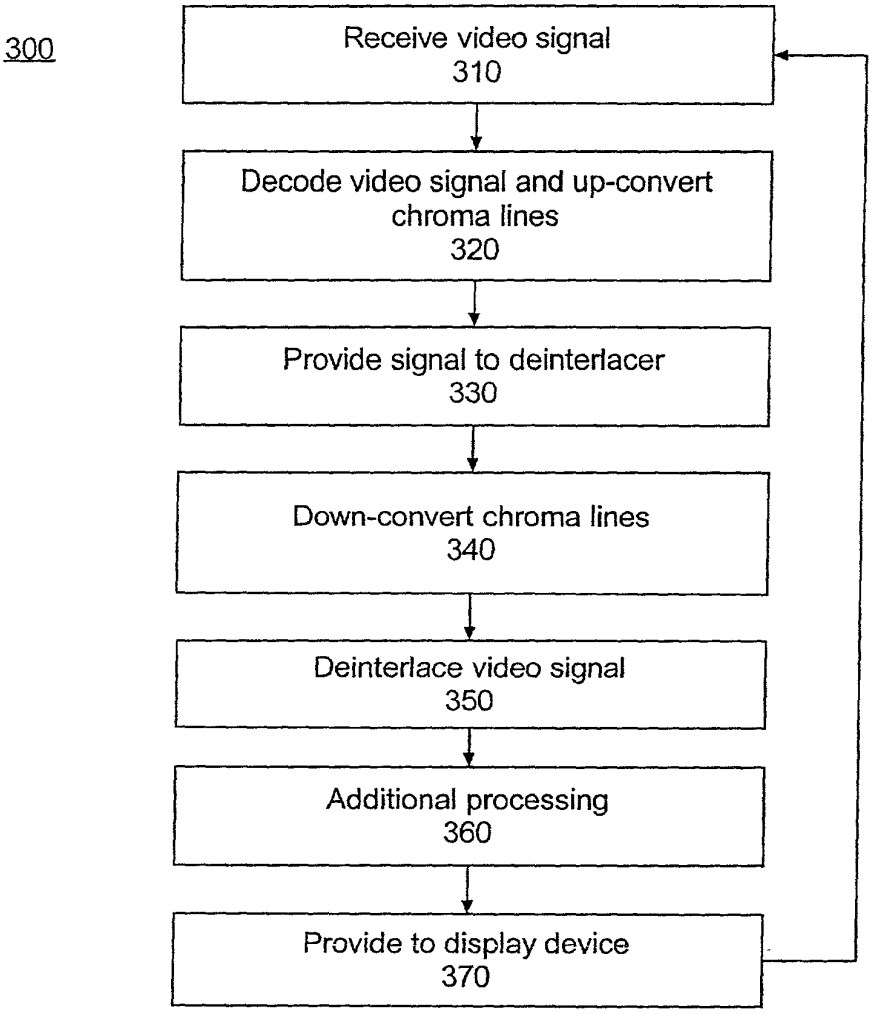
300
Receive video signal
310
Decode video signal and up-convert chroma lines
320
Provide signal to deinterlacer
330
Down-convert chroma lines
340
Deinterlace video signal
350
Additional processing
360
Provide to display device
370

FIGURE 3

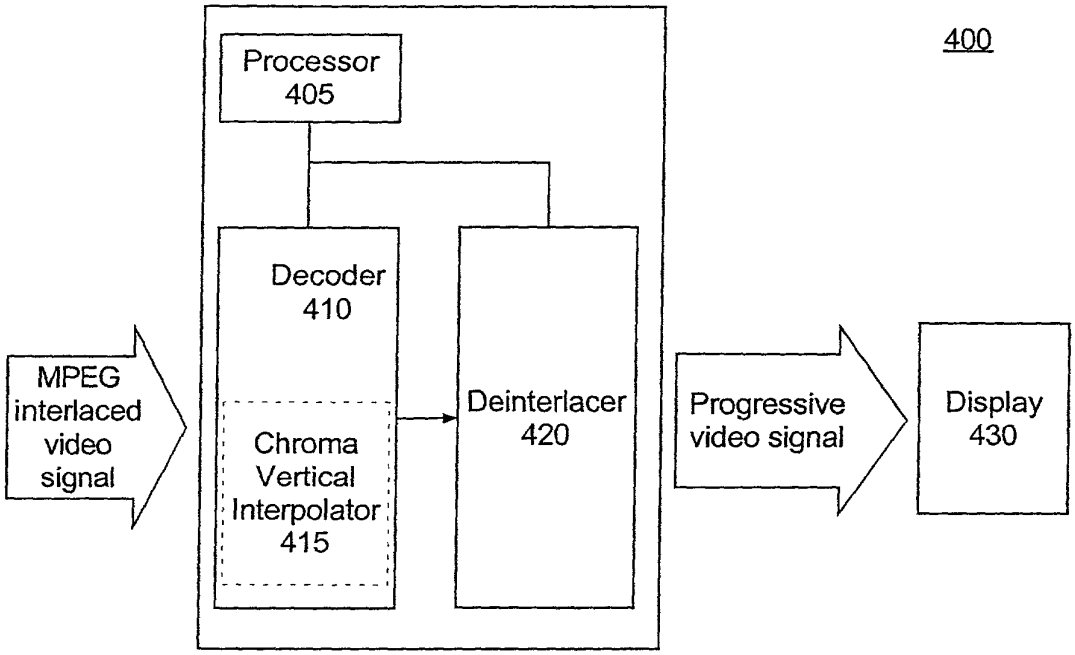
400
Processor
405
Decoder
410
Chroma
Vertical
Interpolator
415
Deinterlacer
420
MPEG
interlaced
video
signal
Progressive
video signal
Display
430

FIGURE 4

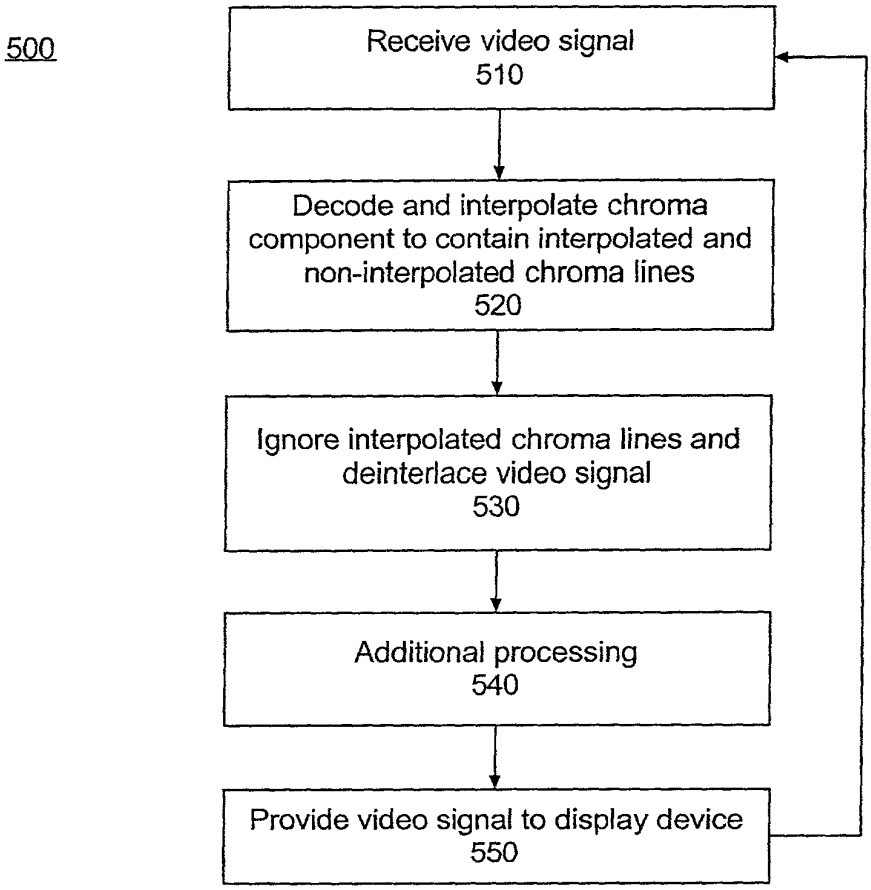
500
Receive video signal
510
Decode and interpolate chroma component to contain interpolated and non-interpolated chroma lines
520
Ignore interpolated chroma lines and deinterlace video signal
530
Additional processing
540
Provide video signal to display device
550

FIGURE 5